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Applicant:

Kenichi Ishibashi et al.

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For:

PHOTOELECTRIC CONVERSION DEVICE, ITS MANUFACTURING

METHOD, ELECTRONIC APPARATUS, ITS MANUFACTURING METHOD.

SEMICONDUCTOR LAYER, AND ITS MANUFACTURING METHOD

CHARACTERISTICS MEASURING METHOD AND AS WELL AS AMPLIFIER

Examiner:

Not yet assigned

Art Unit:

Net yet assigned

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to MAIL STOP MISSING PARTS, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the 8774 day of December, 2005.

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Commissioner For Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

• Transmitted herewith are the following documents:

- Copy of Notification of Missing Requirements under 35 U.S.C. 371
- Declaration and Power of Attorney for Patent Application
- Return Receipt Postcard

If the enclosed papers are considered incomplete, the Mail Room and/or the Application Branch is respectfully requested to contact the undersigned at (617) 646-8000, Boston, Massachusetts.

A check in the amount of \$130.00 is enclosed to cover the filing fee. If the fee is insufficient, the balance may be charged to Deposit Account 23/2825. A duplicate of this sheet is enclosed.

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Respectfully submitted,

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Date: December &

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